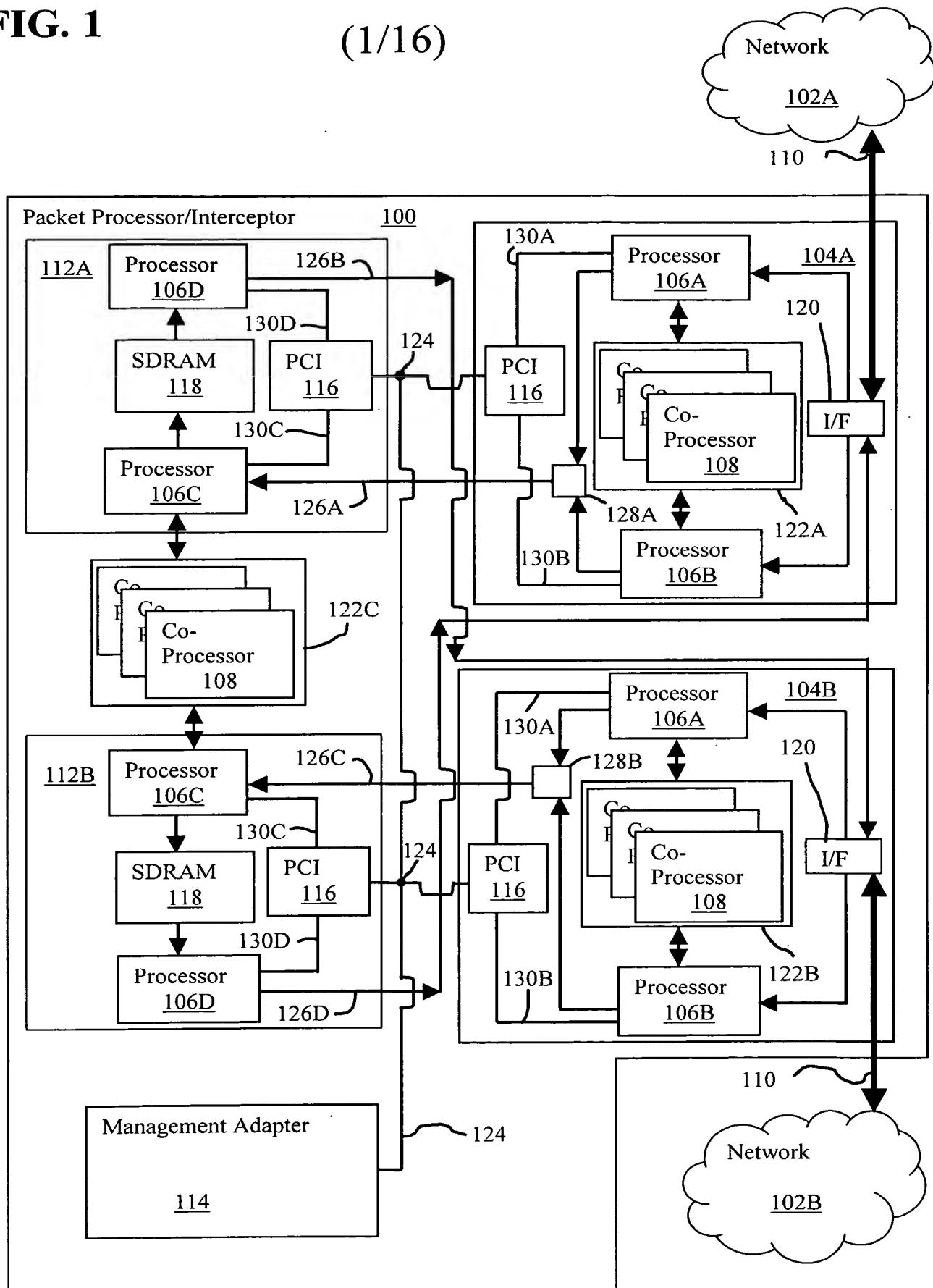
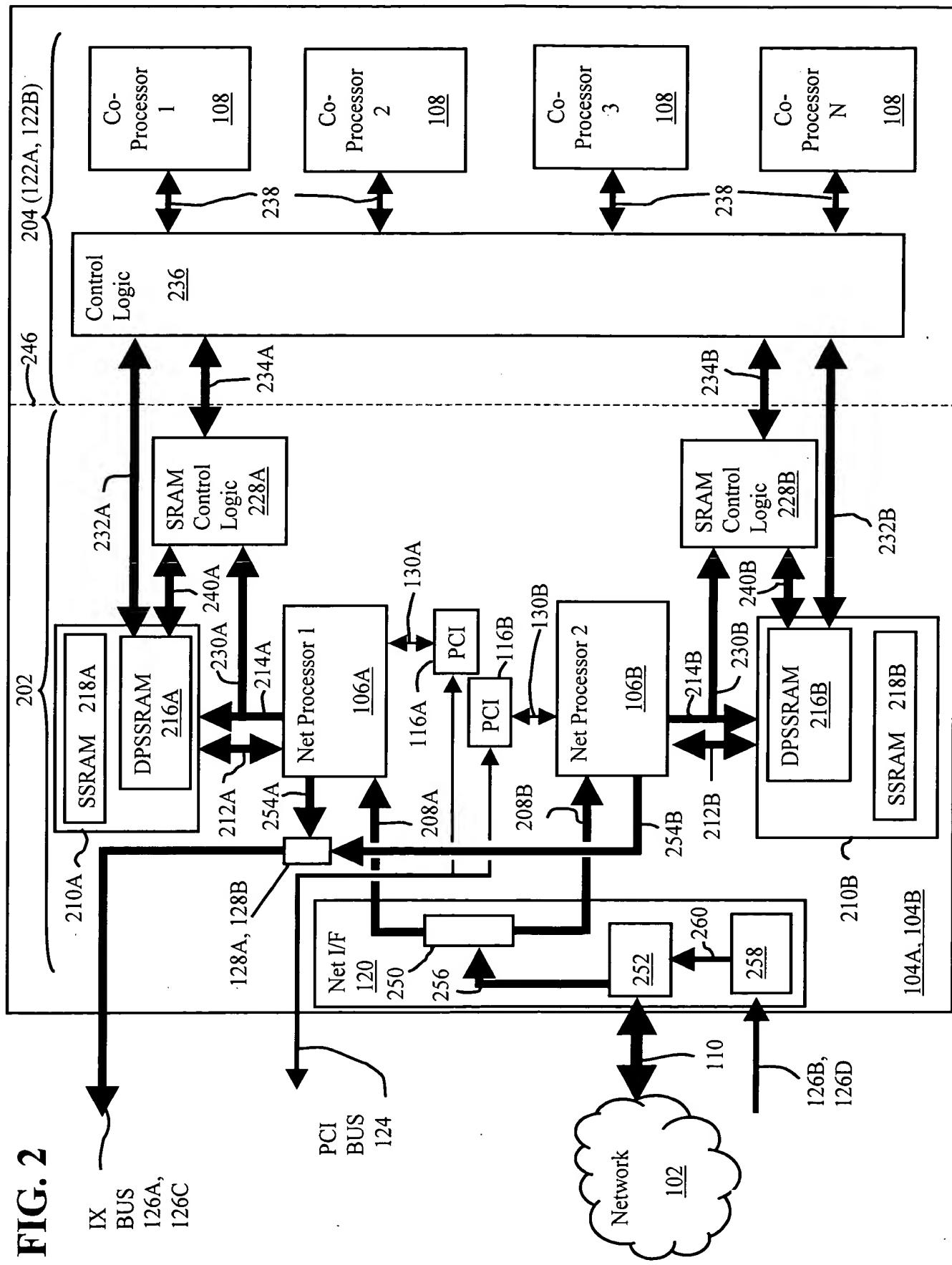


**FIG. 1**  
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Patent Application For: APPARATUS AND METHOD FOR INTERCONNECTING A PROCESSOR TO CO-PROCESSORS USING SHARED MEMORY

Inventor(s): Zahid Najam, Peder J. Jungck, Andrew T. Nguyen

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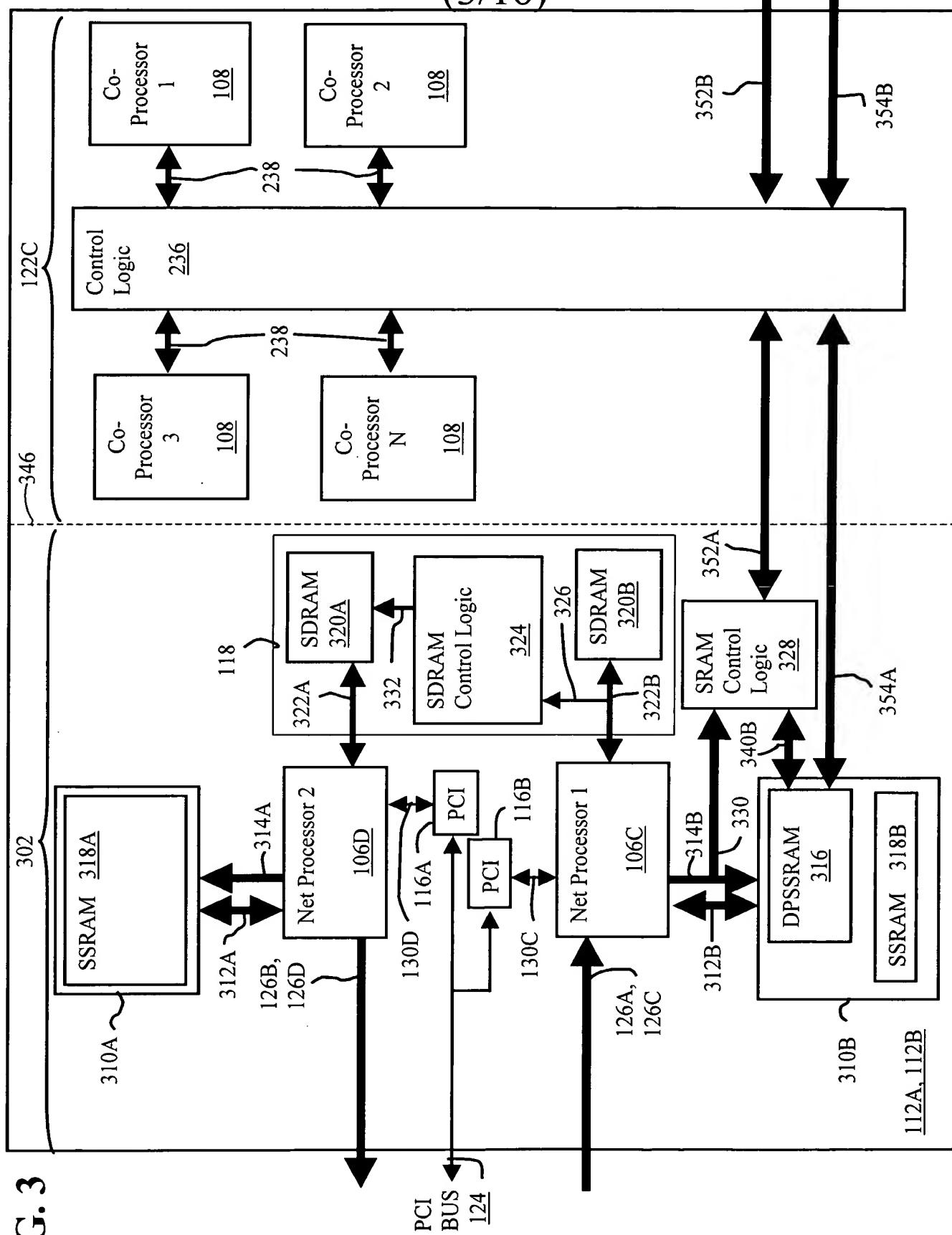
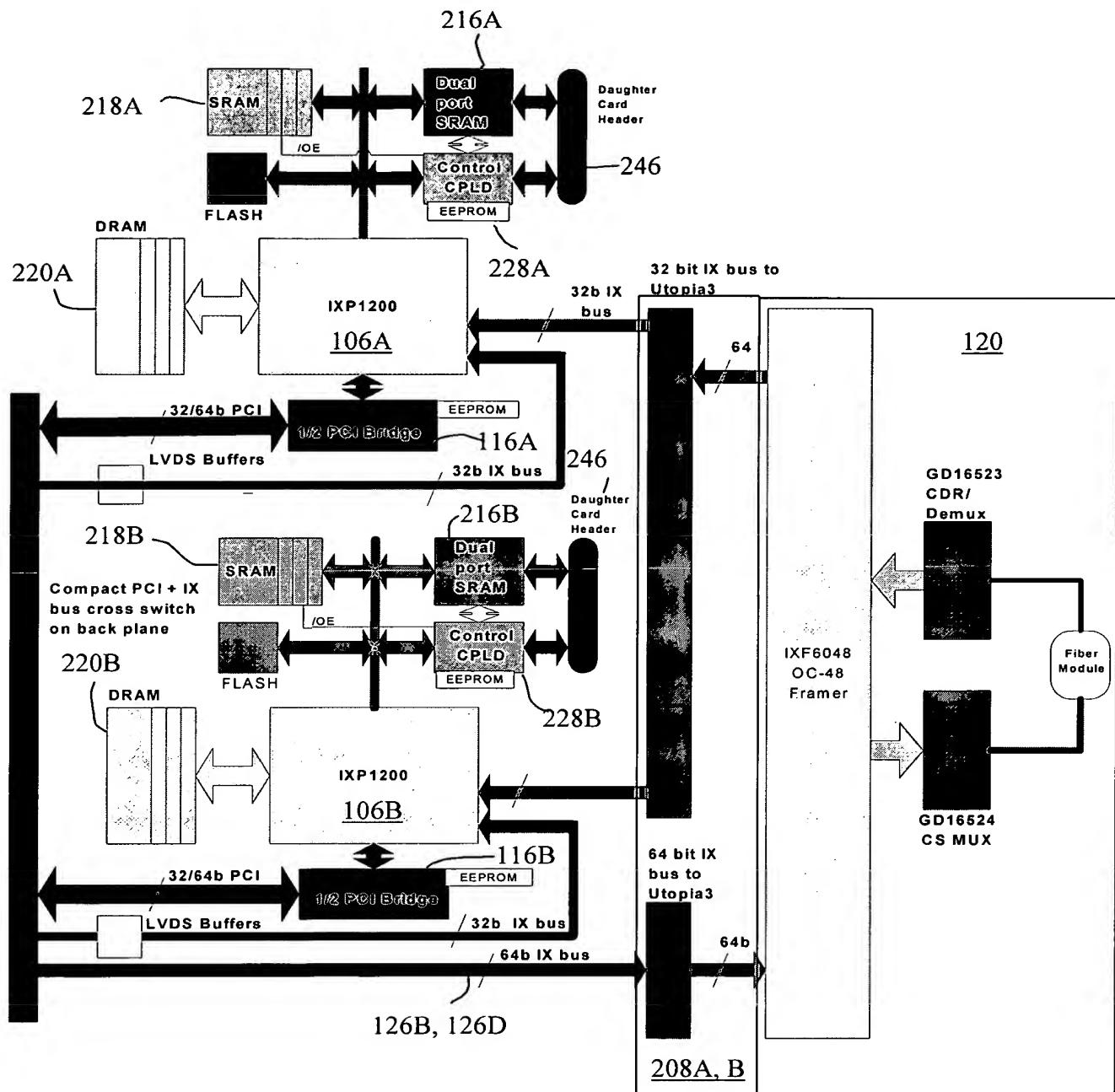


FIG. 3

FIG. 4

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FIG. 4A - FIG. 4D



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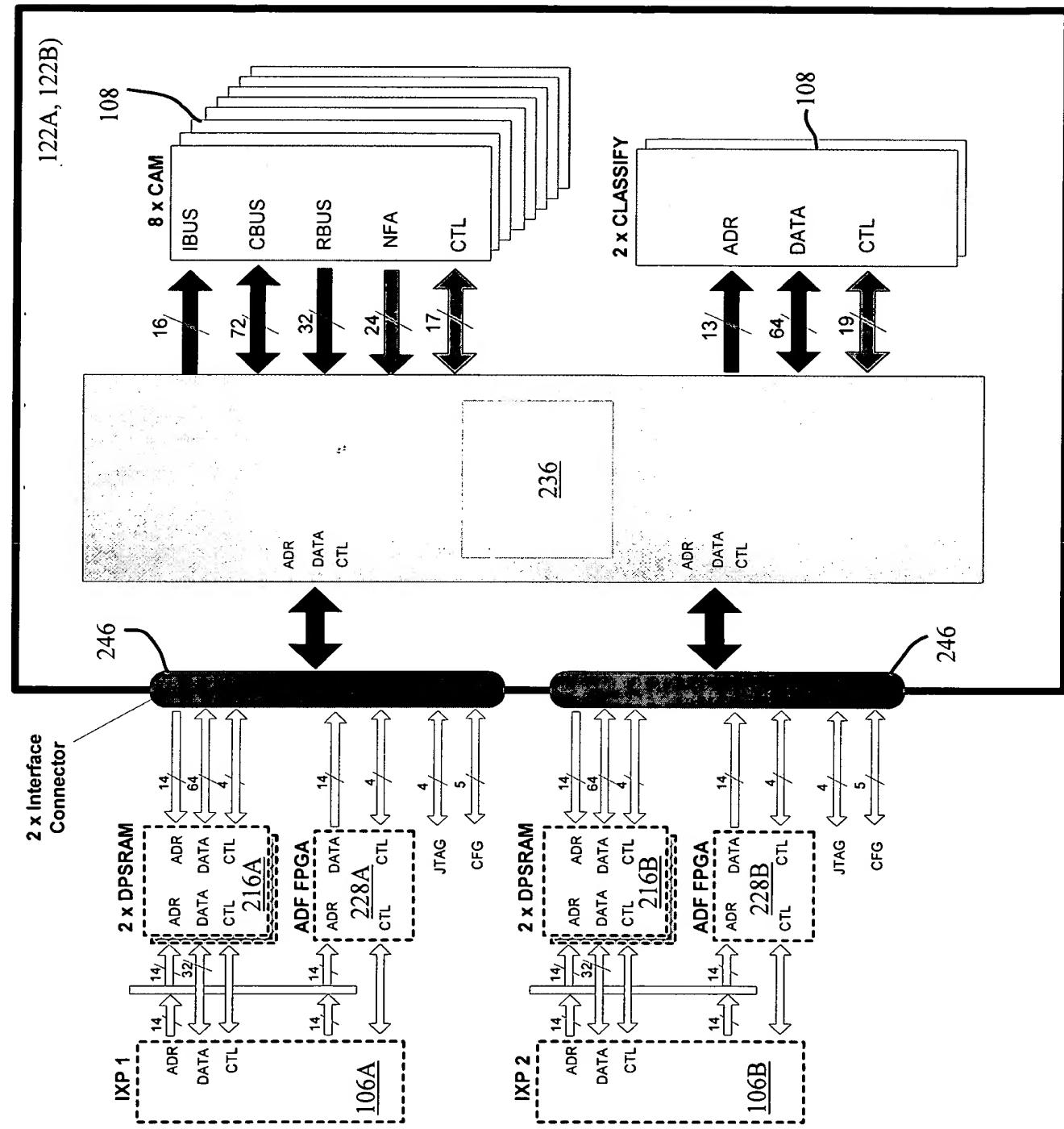
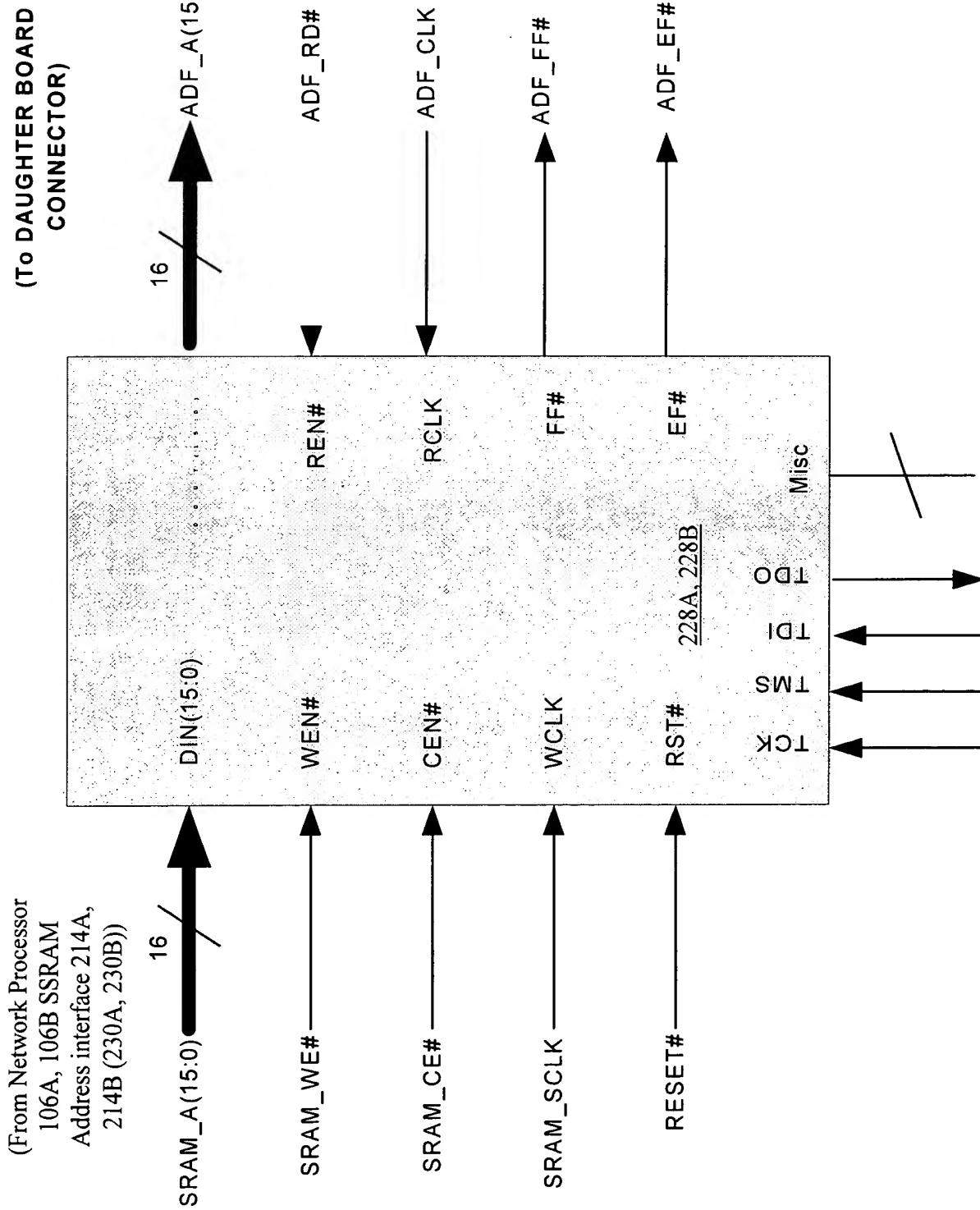


FIG. 5

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FIG. 6

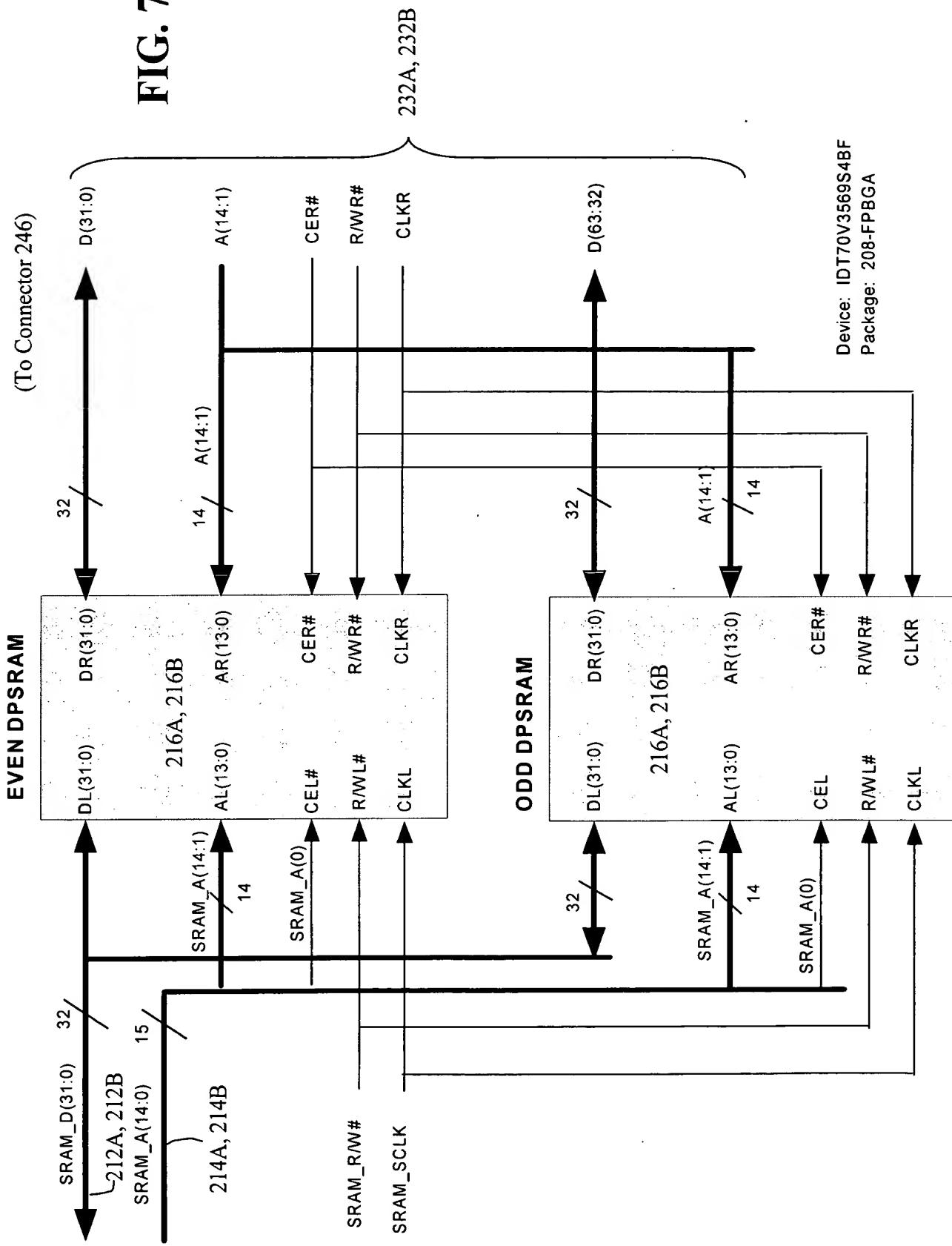


Inventor(s): Zahid Najam, Peder J. Jungck, Andrew T. Nguyen

Attorney Docket No.: 10736/9

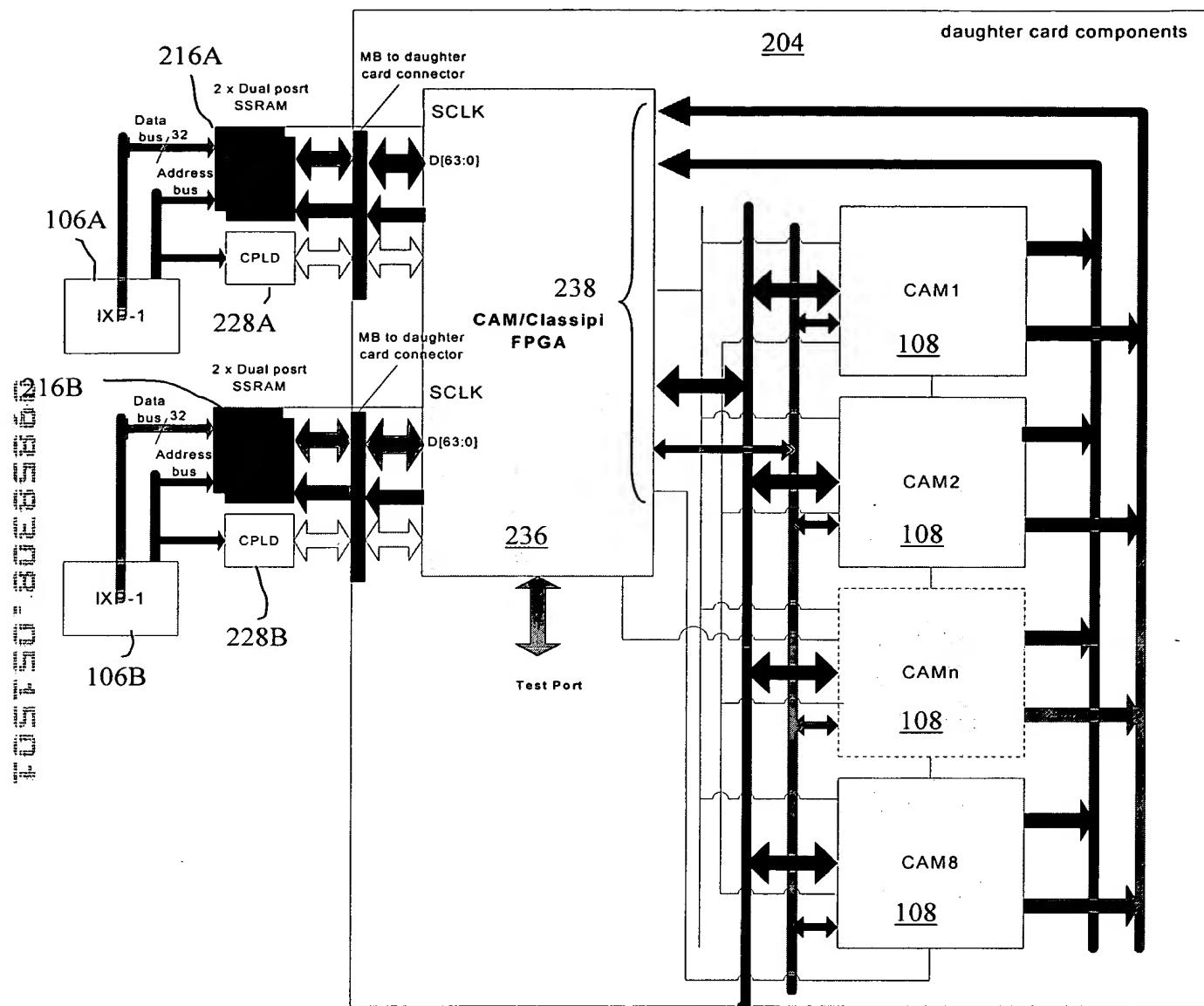
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FIG. 7



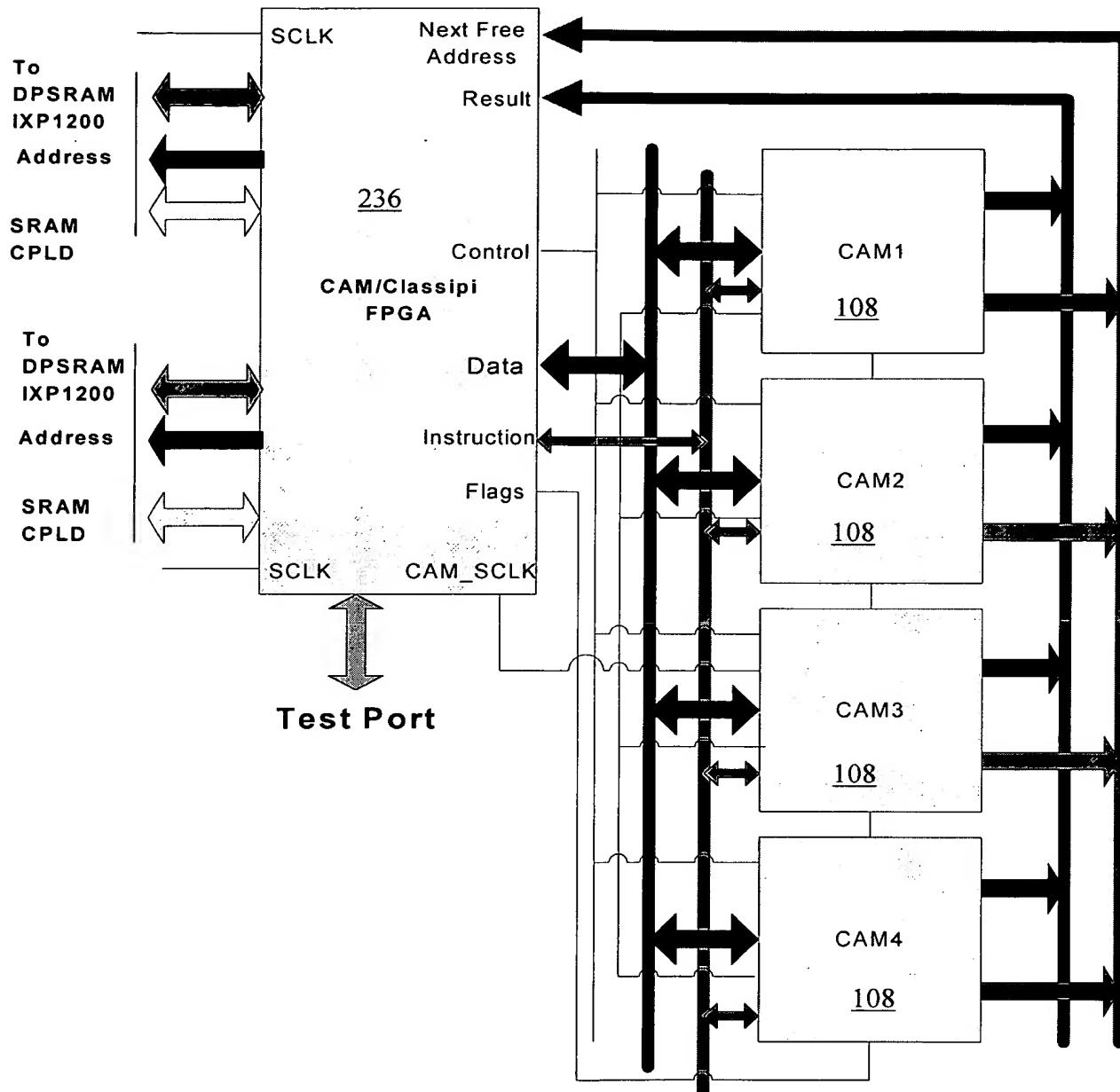
**FIG. 8**

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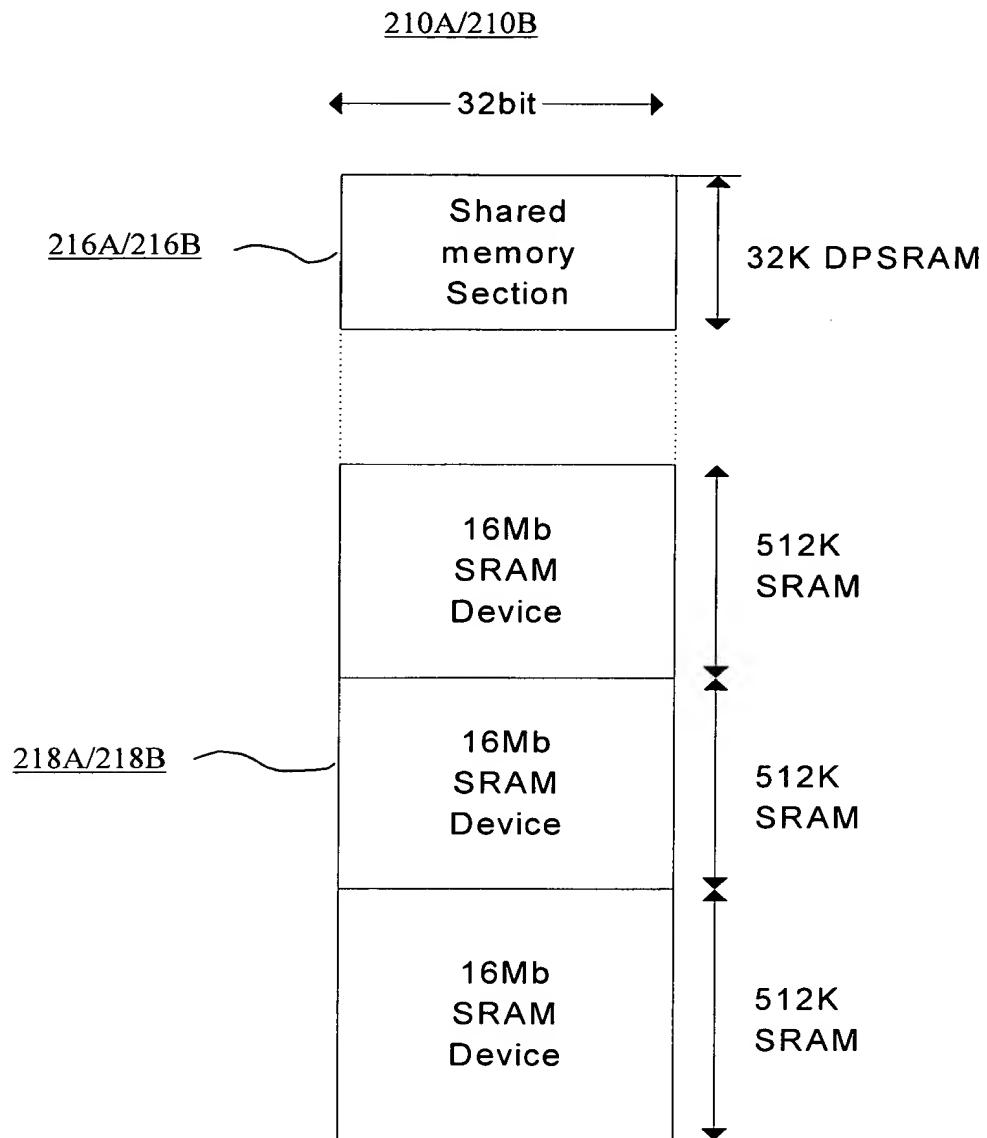


## FIG. 9

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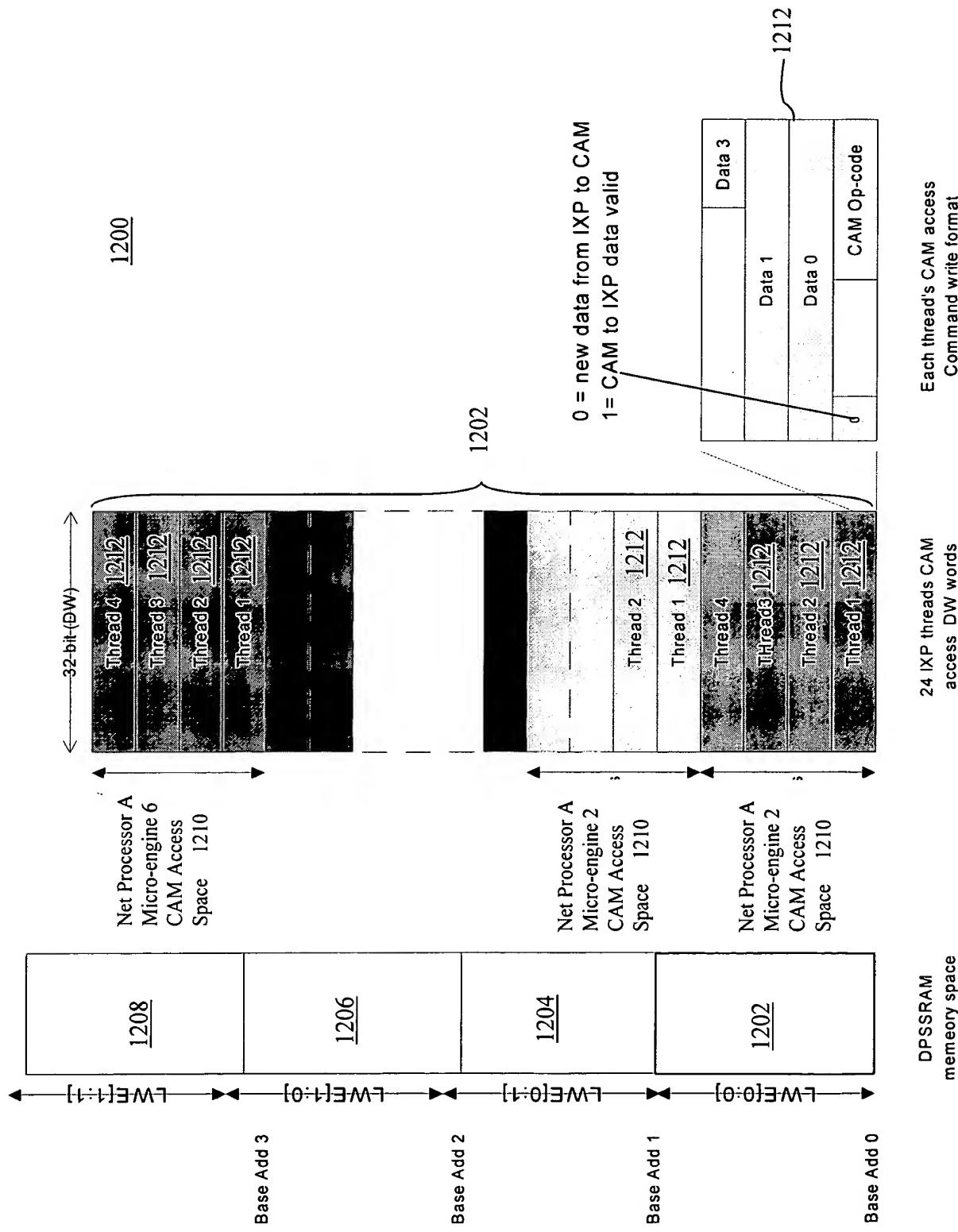
**FIG. 10** (10/16)



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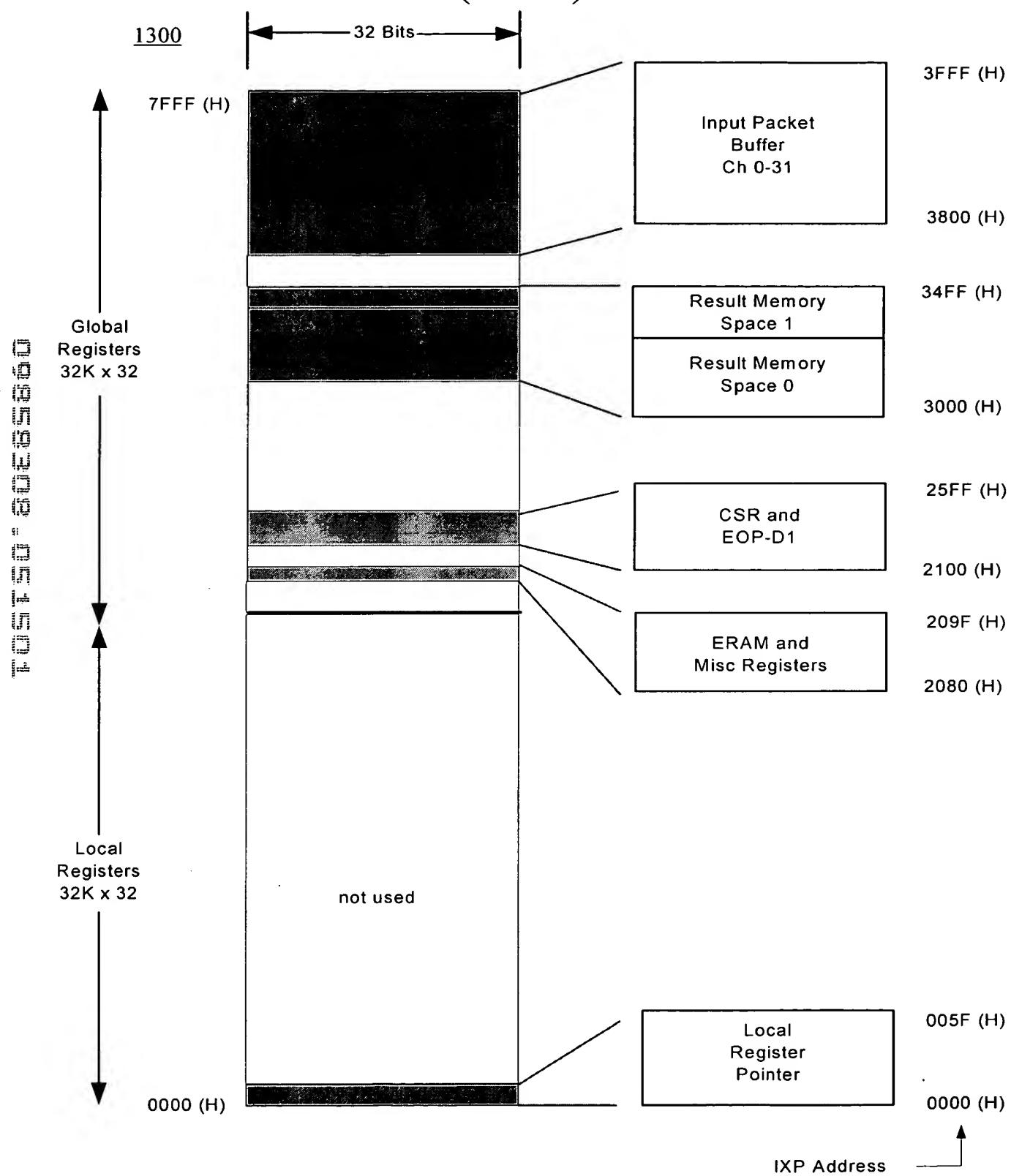
FIG. 11

FIG. 11 - FIG. 11



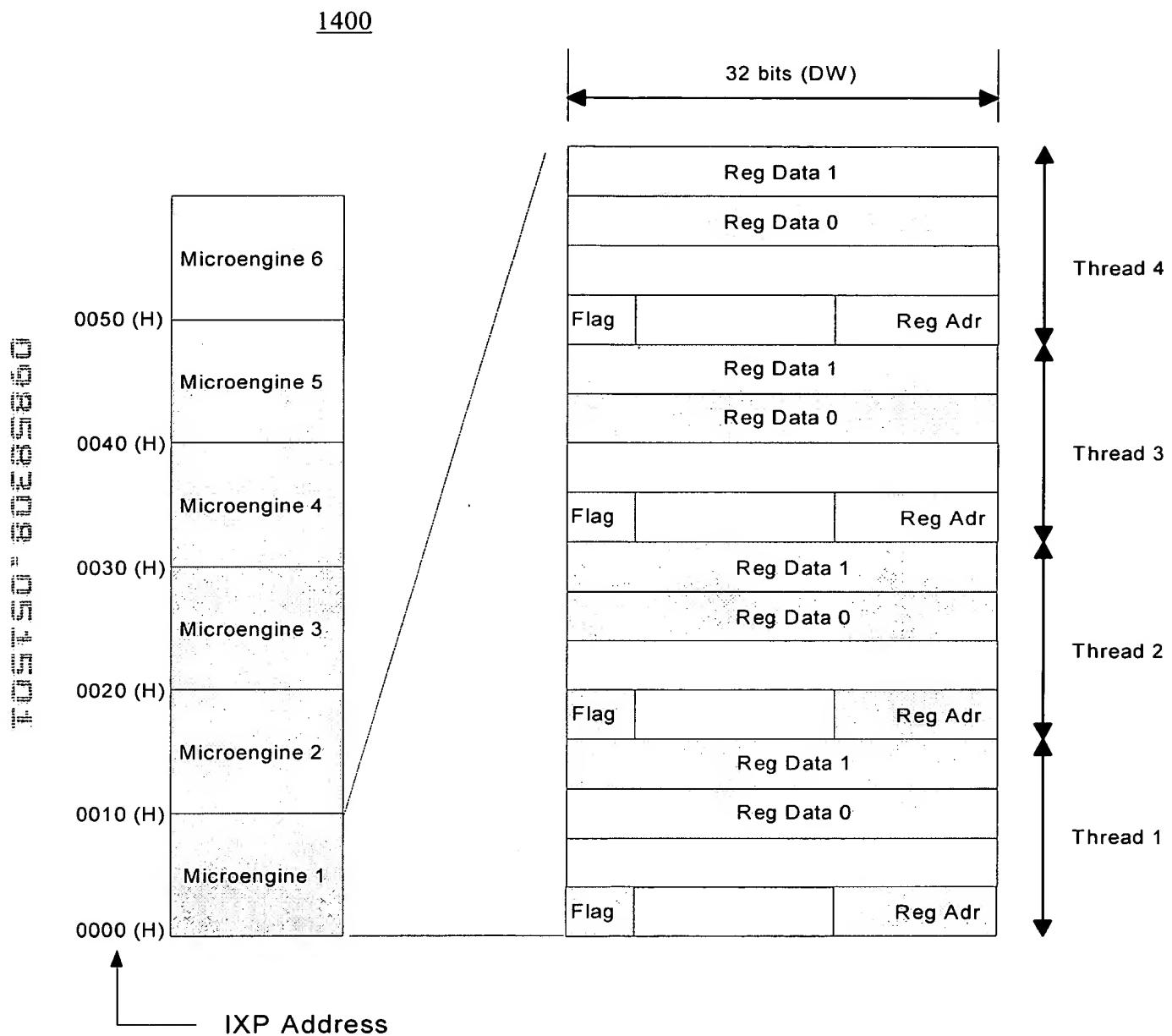
**FIG. 12**

(12/18)



**FIG. 13**

(13/18)



Patent Application For: APPARATUS AND METHOD FOR INTERCONNECTING A PROCESSOR TO CO-  
PROCESSORS USING SHARED MEMORY

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Attorney Docket No.: 10736/9

**FIG. 14**

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1500

7FFF (H)

Channel 32

32 bits (DW)

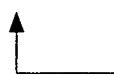
Byte 252	Byte 253	Byte 254	EOP
Byte 248	Byte 249	Byte 250	Byte 251

64 DW

7000 (H)

Channel 4  
Channel 3  
Channel 2  
Channel 1  
Channel 0

Data Byte 4	Data Byte 5	Data Byte 6	Data Byte 7
Data Byte 0	Data Byte 1	Data Byte 2	Data Byte 3

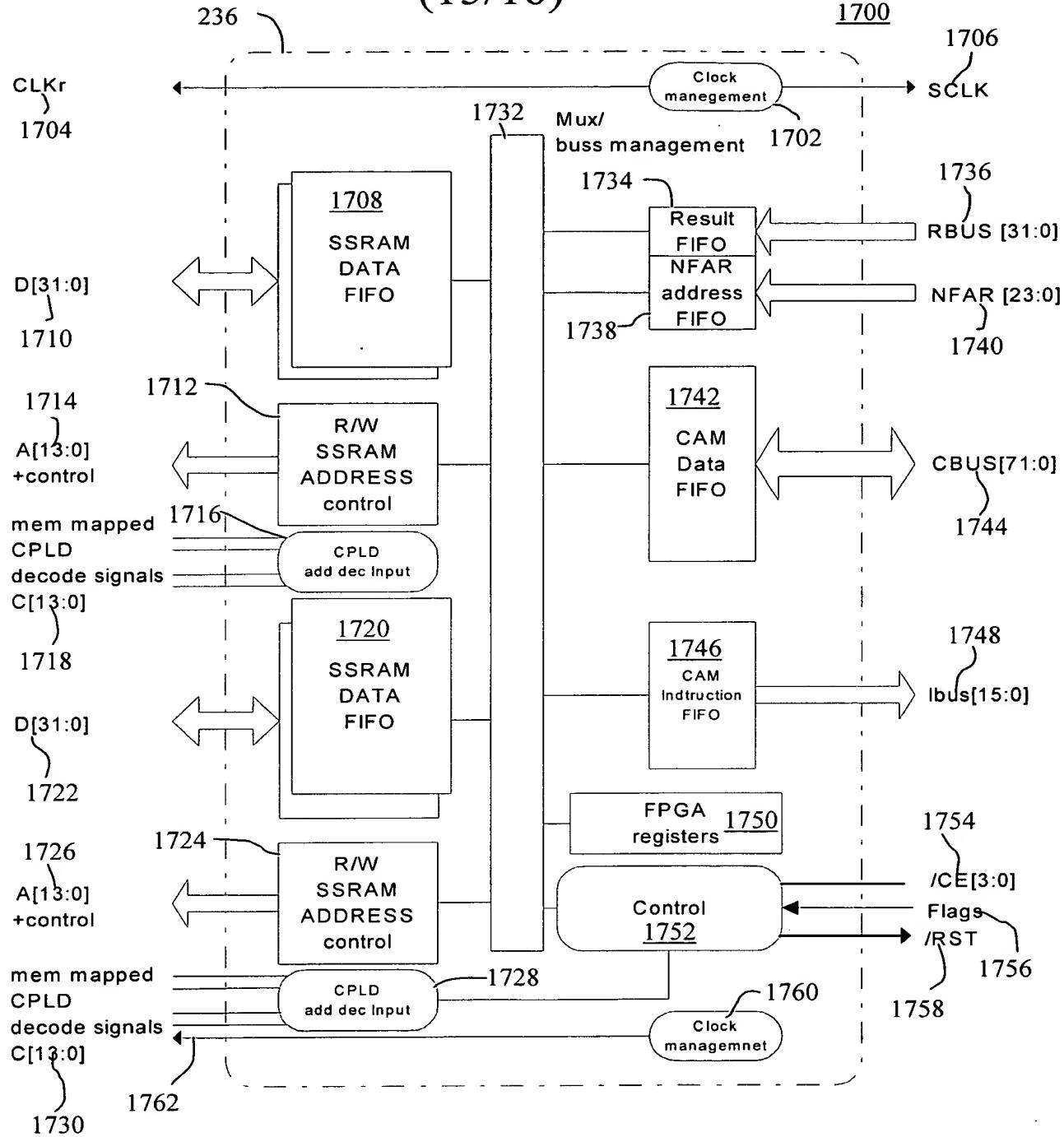


IXP Address

00000000000000000000000000000000

**FIG. 15**

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FIG. 16

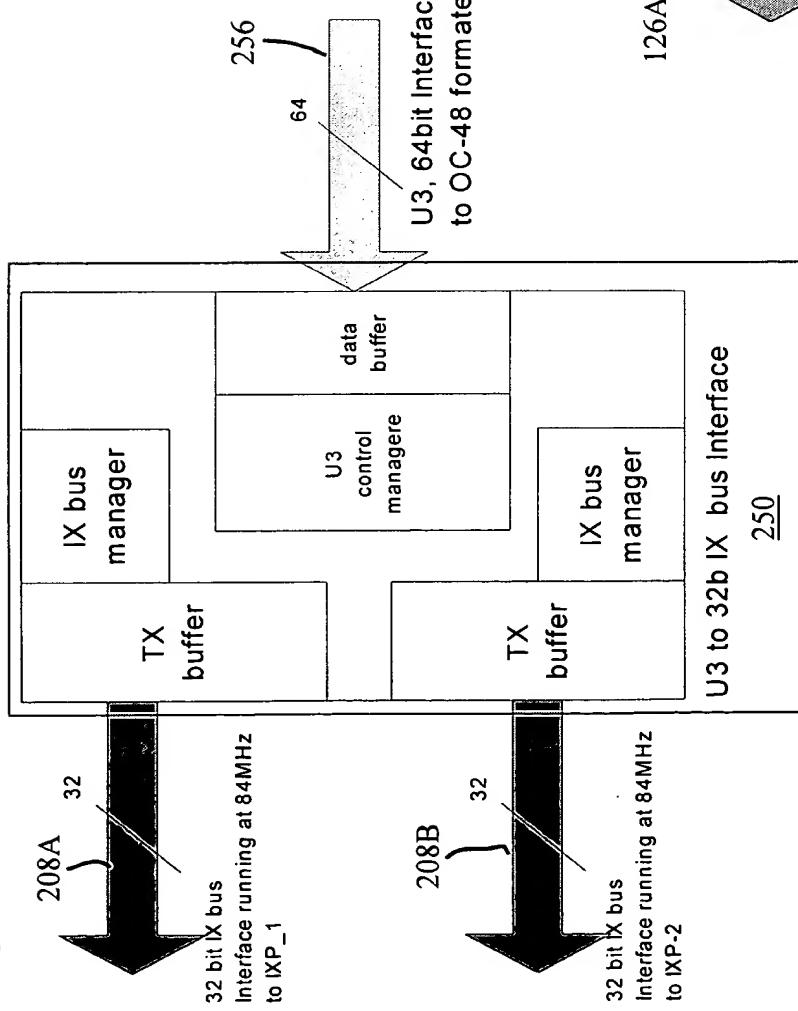


FIG. 17

